

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Bryan D. Sheffield, et al. Art Unit: 2185  
Serial No.: 10/663,144 Examiner: Daniel Y. Kim  
Filed: 09/16/03 Docket: TI-35286  
For: METHOD OF OPERATING A MEMORY AT HIGH SPEED USING A CYCLE  
READY STATUS OUTPUT SIGNAL

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

AMENDMENT

In response to the Office Action in this case mailed May 12, 2006, please enter the following: